	innovations for high performance microelectronics
Ab initio Total Energy Calculat	ions
for Silicon Microelectronic	S
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Agenda	MP
Introduction CMOS technology and MOS	S transistor
Doping of silicon <u>fhimd example</u> : Charge stat <u>fhimd example</u> : Vacancy-as	tes of Si interstitials ssisted diffusion of As
 High-k dielectrics <u>fhimd example</u>: Oxidation o <u>fhimd example</u>: Interfacial s 	of the Pr_2O_3 /Si(001) interface silicate formation in Pr_2O_3 films on Si(001)
Summary	















































